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Jonas Maebe, Dries Buytaert, Lieven Eeckhout, Koen De Bosschere
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programming systems, languages, and applications
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Understanding the behavior of applications running on high-level langua
is non-trivial because of the tight entanglement at the lowest execution
virtual machine. This paper ...

**Keywords:** Java, aspect-oriented instrumentation, customized program

Also published in:
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**2** Topic taxonomy adaptation for group profiling
Lei Tang, Huan Liu, Jianping Zhang, Nitin Agarwal, John J. Salerno
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A topic taxonomy is an effective representation that describes salient fe
communities. A topic taxonomy consists of topic nodes. Each internal n(
ancestor and child nodes) and ...

**Keywords:** Topic taxonomy, dynamic profiling, group interest, taxonon
classification

**3** Improving virtual machine performance using a cross-run profile repo
Matthew Arnold, Adam Welc, V. T. Rajan
October 2005 **OOPSLA '05:** Proceedings of the 20th annual ACM SIGPLAN (
programming, systems, languages, and applications
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Virtual machines for languages such as the Java programming language
dynamic optimization to improve program performance. But despite the
achieving high performance, current ...

**Keywords**: Java, profiling, selective optimization, virtual machine

Also published in:
　October 2005 **SIGPLAN Notices** Volume 40 Issue 10

**4** Cache-conscious data placement
Brad Calder, Chandra Krintz, Simmi John, Todd Austin
December 1998 **ASPLOS-VIII:** Proceedings of the eighth international conf
　　　　　　　　programming languages and operating systems
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As the gap between memory and processor speeds continues to widen,
component of processor performance. Compiler techniques have been u
pet$ormance by mapping code with temporal ...

Also published in:
　November 1998 **SIGPLAN Notices**　　　　　　　Volume 33 Issue 11
　December 1998 **SIGOPS Operating Systems Review** Volume 32 Issue 5

**5** Virtual reuse distance analysis of SPECjvm2008 data locality
Xiaoming Gu, Xiao-Feng Li, Buqi Cheng, Eric Huang
August 2009　**PPPJ '09:** Proceedings of the 7th International Conference on
　　　　　　　Java
**Publisher:** ACM　Request Permissions
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Reuse distance analysis has been proved promising in evaluating and pi
in Fortran or C/C++. But its effect has not been examined for applicatio
where there is no concept of ...

**Keywords**: SPECjvm2008, data locality, managed runtime, reuse dista

**6** Scratchpad memory management for portable systems with a memo
Bernhard Egger, Jaejin Lee, Heonshik Shin
October 2006　**EMSOFT '06:** Proceedings of the 6th ACM & IEEE Internation
**Publisher:** ACM　Request Permissions
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**Bibliometrics**: Downloads (6 Weeks): 5,　Downloads (12 Months): 75,　Downloa

In this paper,we present a dynamic scratchpad memory allocation strat memory subsystem for contemporary embedded processors. The memc management unit (MMU), and physically ...

**Keywords**: code placement, compilers, heterogeneous memory, paging scratchpad, virtual memory

**7** Profile-driven code unloading for resource-constrained JVMs

Lingli Zhang, Chandra Krintz

June 2004    **PPPJ '04:** Proceedings of the 3rd international symposium on Java

**Publisher:** Trinity College Dublin

Full text available: Pdf (99.23 KB)                    Additional Information: full citation, abstr

**Bibliometrics**: Downloads (6 Weeks): 7,  Downloads (12 Months): 18,  Downloa

Java virtual machines (JVMs) have become increasingly popular for exer mobile and embedded devices. Most JVMs for such devices execute prog that use dynamic compilation have ...

**8** On the predictability of Java byte codes (abstract) (poster session)

Karel Driesen, Patrick Lam, Jerome Miecznikowski, Feng Qian, Derek Raysi

January 2000  **OOPSLA '00:** Addendum to the 2000 proceedings of the cont systems, languages, and applications (Addendum)

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*Java byte codes are platform-independent. That means that any charac code execution level will reveal characteristics that any Java Virtual Mac whether this JVM is a ...*

**9** STEP: a framework for the efficient encoding of general trace data

Rhodes Brown, Karel Driesen, David Eng, Laurie Hendren, John Jorgensen,

January 2003 **PASTE '02:** Proceedings of the 2002 ACM SIGPLAN-SIGSOFT software tools and engineering

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Traditional tracing systems are often limited to recording a fixed set of l frustrate an application or compiler developer who is trying to understai of software systems such ...

**Keywords**: data definition language, program event trace, sequential c

Also published in:
    January 2003 **SIGSOFT Software Engineering Notes** Volume 28 Issue 1

**10**

Hardware profile-guided automatic page placement for ccNUMA sys
Jaydeep Marathe, Frank Mueller
March 2006    **PPoPP '06:** Proceedings of the eleventh ACM SIGPLAN sympo
programming
**Publisher:** ACM    Request Permissions
Full text available: Pdf (167.64 KB)                Additional Information: full citation, abstr

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Cache coherent non-uniform memory architectures (ccNUMA) constitute
computing plat-forms. Contemporary ccNUMA systems, such as the SGI
where each node consists of a small number ...

**Keywords:** NUMA, hardware performance monitoring, page placement,

**11** Strategic decisions on technology selections for facilitating a network
& total cost of ownership theories
Kimfong Lei, Phillip T. Rawles
October 2003  **CITC4 '03:** Proceedings of the 4th conference on Information
**Publisher:** ACM    Request Permissions
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**Bibliometrics:** Downloads (6 Weeks): 10,   Downloads (12 Months): 107,   Downl

This paper addresses the selection of technologies that provide each stu
non-dedicated host machine. The authors investigated different combina
approaches, such as virtual machine ...

**Keywords:** VMware, course development, curriculum, end-user compu
interesting applications in IT, networking, operating systems, systems s

**12** Structure and application of a measurement tool-SAMPLER/3000
Abbas Rafii
September 1981  **SIGMETRICS '81:** Proceedings of the 1981 ACM SIGMETF
modeling of computer systems
**Publisher:** ACM    Request Permissions
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**Bibliometrics:** Downloads (6 Weeks): 2,   Downloads (12 Months): 8,   Download

Design, internal structure, implementation experience and a number of
performance evaluation tool are presented. This package can be used to
profiles in several levels of detail in ...

Also published in:
September 1981 **SIGMETRICS Performance Evaluation Review** Volume 10 Is

**13** Construction engineering and project management I: variable-speed
discrete-event process models
Vineet R. Kamat, Julio C. Martinez
December 2003 **WSC '03:** Proceedings of the 35th conference on Winter sir
**Publisher:** Winter Simulation Conference

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This paper presents research that addresses the problem of describing t
simulation objects on realistically-shaped trajectories (i.e. paths) in anir
models. The work puts in place ...

**14** IRIS performer: a high performance multiprocessing toolkit for real-ti
John Rohlf, James Helman
July 1994      **SIGGRAPH '94:** Proceedings of the 21st annual conference o
techniques
**Publisher:** ACM 🔷 Request Permissions
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This paper describes the design and implementation of IRIS Performer,
reality, and other real-time 3D graphics applications. The principal desig
more easily obtain maximal ...

**15** Prefetch injection based on hardware monitoring and object metada
Ali-Reza Adl-Tabatabai, Richard L. Hudson, Mauricio J. Serrano, Sreenivas
June 2004      **PLDI '04:** Proceedings of the ACM SIGPLAN 2004 conference
implementation
**Publisher:** ACM 🔷 Request Permissions
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Cache miss stalls hurt performance because of the large gap between m
example, the popular server benchmark SPEC JBB2000 spends 45% of
requests on the Itanium® 2 processor. Traversing ...

**Keywords:** cache misses, compiler optimization, garbage collection, pr
virtual machines

Also published in:
June 2004 **SIGPLAN Notices** Volume 39 Issue 6

**16** Profiling Java applications using code hotswapping and dynamic call
Mikhail Dmitriev
January 2004 **WOSP '04:** Proceedings of the 4th international workshop on
**Publisher:** ACM 🔷 Request Permissions
Full text available: 📄 Pdf (1.32 MB)          Additional Information: full citation, abstr

**Bibliometrics**: Downloads (6 Weeks): 10,   Downloads (12 Months): 75,   Downlo

Instrumentation-based profiling has many advantages and one serious (
overhead. This overhead can be substantially reduced if only a small pa
one that has previously been ...

Also published in:

January 2004 **SIGSOFT Software Engineering Notes** Volume 29 Issue 1

**17** Efficient memory management for hardware accelerated Java Virtua
Peter Bertels, Wim Heirman, Erik D'Hollander, Dirk Stroobandt
August 2009  **Transactions on Design Automation of Electronic Syster**
**Publisher:** ACM  Request Permissions
Full text available:  Pdf (1.32 MB)          Additional Information: full citation, abstr

**Bibliometrics**: Downloads (6 Weeks): 13,  Downloads (12 Months): 134,  Downl

Application-specific hardware accelerators can significantly improve a sy
system, we then have to consider a hybrid architecture that consists of
general-purpose processor connected ...

**Keywords**: Dynamic memory management, Java Virtual Machine, hard

**18** How java programs interact with virtual machines at the microarchite
Lieven Eeckhout, Andy Georges, Koen De Bosschere
November 2003  **OOPSLA '03:** Proceedings of the 18th annual ACM SIGPLA
programing, systems, languages, and applications
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**Bibliometrics**: Downloads (6 Weeks): 7,  Downloads (12 Months): 81,  Downloa

Java workloads are becoming increasingly prominent on various platforr
general-purpose computers to high-end servers. Understanding the imp
running Java workloads, is thus ...

**Keywords**: Java workloads, performance analysis, statistical data anal
characterization

Also published in:
November 2003 **SIGPLAN Notices** Volume 38 Issue 11

**19** Mixed mode execution with context threading
Mathew Zaleski, Marc Berndl, Angela Demke Brown
October 2005  **CASCON '05:** Proceedings of the 2005 conference of the Cen
research
**Publisher:** IBM Press
Full text available:  Pdf (162.98 KB)          Additional Information: full citation, abstr

**Bibliometrics**: Downloads (6 Weeks): 2,  Downloads (12 Months): 15,  Downloa

Interpreters are widely used to implement portable language runtime er
languages may benefit from performance beyond that obtainable by opt
high-performance mixed-mode virtual ...

**20** Trace-driven memory simulation: a survey
Richard A. Uhlig, Trevor N. Mudge
June 1997    **Computing Surveys (CSUR)** , Volume 29 Issue 2
**Publisher:** ACM  Request Permissions

Full text available: Pdf (636.11 KB)          Additional Information: full citation, abstr

**Bibliometrics**: Downloads (6 Weeks): 11,   Downloads (12 Months): 218,   Downl

As the gap between processor and memory speeds continues to widen,
designs before they are implemented in hardware are becoming increas
driven memory simulation, has been the ...

**Keywords**: TLBs, caches, memory management, memory simulation, t

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